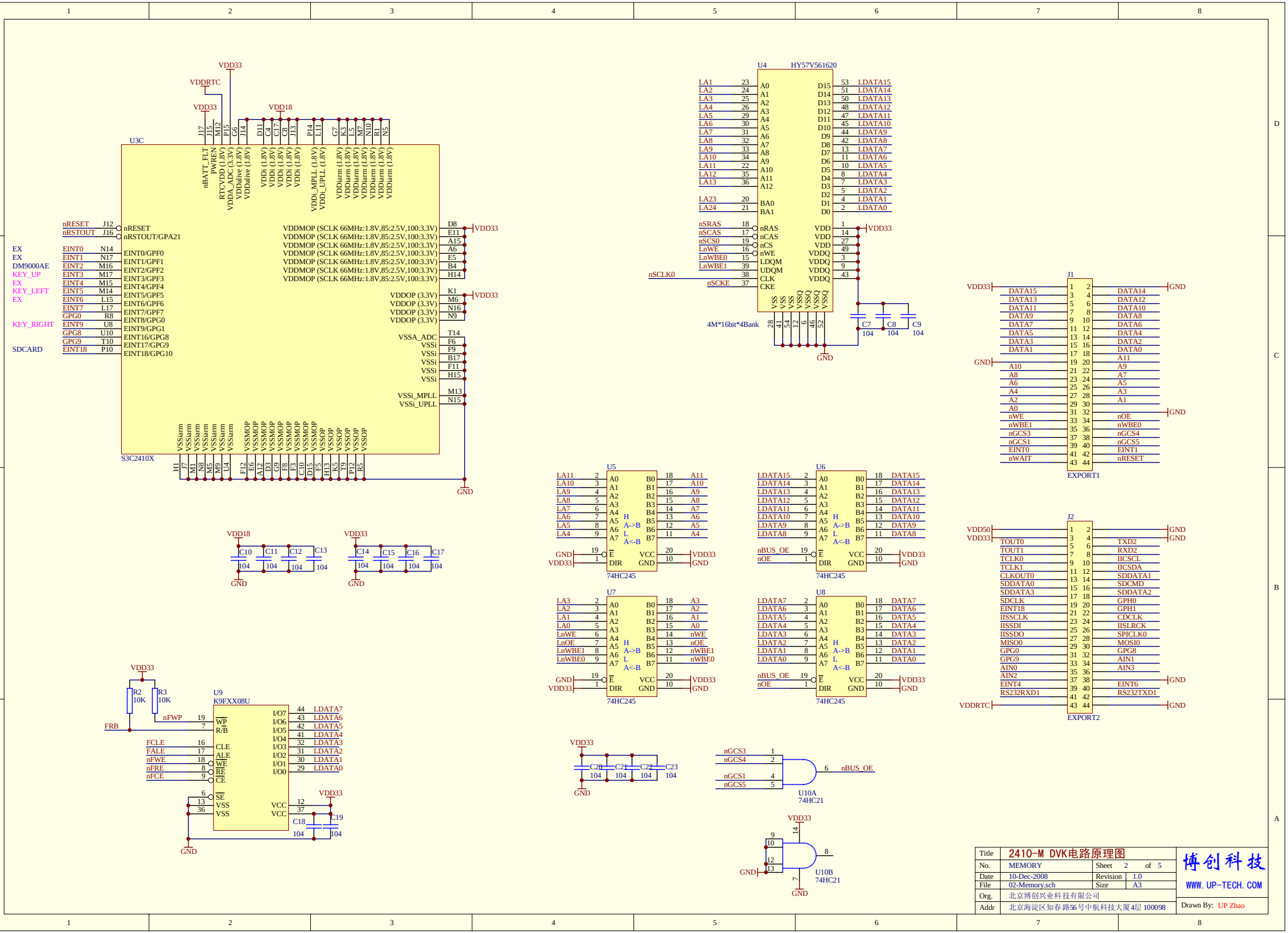
U3C
S3C2410X
U4 HY57V561620
4M*16bit*4Bank
J1 EXPORT1
J2 EXPORT2
U5 74HC245
U6 74HC245
U7 74HC245
U8 74HC245
U9 K9FXX08U
U10A 74HC21
U10B 74HC21
VDD33
VDDRTC
VDD18
GND
EX
DM9000AE
KEY_UP
KEY_LEFT
KEY_RIGHT
SDCARD
Title 2410-M DVK电路原理图
No. MEMORY
Sheet 2 of 5
Date 10-Dec-2008
Revision 1.0
File 02-Memory.sch
Size A3
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